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455610-2490**IN THE CLAIMS:**

The following listing of the claims replaces and supersedes all prior claim listings.
Please amend the claims as set out below.

1. (Currently Amended) A method for generating a graphical representation of a processing web by an oscilloscope for implementation and control of the oscilloscope, comprising the steps of:

determining a first processing element of said processing web corresponding to a first processing apparatus of the oscilloscope for processing a signal;

placing said first processing element in a particular location based at least in part upon its location in said processing web and various inputs to and outputs from said first processing element;

determining a second processing element of said processing web corresponding to a second processing apparatus of the oscilloscope for processing a signal;

placing said second processing element in a particular location downstream from said first processing element based at least in part upon its location in said processing web, various inputs to and outputs from said second processing element, and a relationship between said second processing element and said first processing element; and

graphically connecting at least one pin of said first processing element to one pin of said second processing element so that the signal processed by in response to a request for the signal passed upstream from said second processing element -the first processing element is forwarded processes and forwards the signal to the second processing element-upon a request from said second processing element to said first processing element for said signal.

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2. (Original) The method of claim 1, wherein said connecting step connects an output pin of said first element to an input pin of said second element.

3. (Previously Presented) The method of claim 2, wherein said connecting step generates a line in said graphical representation between said output pin of said first element to said input pin of said second element.

4. (Original) The method of claim 3, wherein said line is drawn including one of a plurality of designations based upon a type of data being carried thereon.

5. (Original) The method of claim 4, wherein said plurality of designations are colors.

6. (Original) The method of claim 1 wherein said at least one pin of said first processing element and said at least one pin of said second processing element are coded based upon a type of data to output therefrom, or received thereby, respectively.

7. (Original) The method of claim 6, wherein said coding is by color.

8. (Original) The method of claim 6, wherein said coding is by symbol.

9. (Original) The method of claim 6, wherein said coding is by graphical designation.

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10. (Original) The method of claim 1, wherein said first processing element is updated at a faster rate and said second processing element is updated at a slower rate.

11. (Previously Presented) The method of claim 10, wherein said update of said first processing element and update of said second processing element are synchronized.

12. (Previously Presented) The method of claim 10, wherein said update of said first and second processing elements is controlled by an update processing element.

13. (Original) The method of claim 1, wherein a viewing object may be placed at any location on the graphical representation to see a current, live output at that location.

14. (Currently Amended) A method for generating a graphical representation of a processing web by an oscilloscope for implementation and control of the oscilloscope, comprising the steps of:

determining a first processing element of said processing web corresponding to a first processing apparatus of the oscilloscope for processing a signal;

placing said first processing element in a particular location based at least in part upon its function and various inputs to and outputs from said first processing element;

determining a second processing element of said processing web corresponding to a second processing apparatus of the oscilloscope for processing a signal;

placing said second processing element in a particular location downstream from said first processing element based at least in part upon its function, various inputs to and outputs

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from said second processing element, and a relationship between said second processing element and said first processing element; and

graphically connecting said first processing element to said second processing element indicating a flow of data therebetween so that the signal processed by in response to a request for the signal passed upstream from said second processing element the first processing ~~apparatus~~ element ~~is forwarded~~ processes and forwards the signal to the second processing ~~apparatus~~ element ~~upon a request from said second processing element to said first processing element for said signal;~~

wherein said first processing element is a waveform acquisition processing element; and wherein said second processing element is a display processing element.

15. (Currently Amended) The method of claim 14, further comprising the steps of:

determining a third processing element of said processing web corresponding to a third processing apparatus of the oscilloscope for processing a signal; and

placing said third processing element in a particular location downstream from said first processing element and upstream from said second processing element based at least in part upon its function in said processing web, various inputs to and outputs from said second processing element, and a relationship between said third processing element and said first and second processing elements so that the signal processed by the first processing ~~apparatus~~ element is forwarded to the third processing ~~apparatus~~ element, and the signal processed by the third processing ~~apparatus~~ element is forwarded to the second processing ~~apparatus~~ element;

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wherein said third processing element performs an intermediate processing step between said first processing element and said second processing element in response to a request for processing from said second processing element.

16. (Currently Amended) The method of claim 14, further comprising the steps of:
determining a third processing element of said processing web corresponding to a third processing apparatus of the oscilloscope for processing a signal;

placing said third processing element in a particular location downstream from said first processing element and upstream from said second processing element based at least in part upon its function in said processing web, various inputs to and outputs from said second processing element, and a relationship between said third processing element and said first and second processing elements so that the signal processed by the first processing ~~apparatus~~ element is forwarded to the third processing ~~apparatus~~ element in response to a request for processing from said third processing element, and the signal processed by the third processing ~~apparatus~~ element is forwarded to the second processing ~~apparatus~~ element in response to a request for processing from said second processing element;

wherein said third processing element is a static memory input.

17. (Currently Amended) The method of claim 14, further comprising the steps of:
determining a third processing element of said processing web corresponding to a third processing apparatus of the oscilloscope for processing a signal;

placing said third processing element in a particular location downstream from said first processing element and upstream from said second processing element based at least in part upon

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its function in said processing web, various inputs to and outputs from said second processing element, and a relationship between said third processing element and said first and second processing elements so that the signal processed by the first processing ~~apparatus~~ element is forwarded to the third processing ~~apparatus~~ element in response to a request for processing from said third processing element, and the signal processed by the third processing ~~apparatus~~ element is forwarded to the second processing ~~apparatus~~ element in response to a request for processing from said second processing element;

wherein said third processing element is a display trace output including at least one processing function.

18. (Currently Amended) The method of claim 14, further comprising the steps of:

determining a third processing element of said processing web corresponding to a third processing apparatus of the oscilloscope for processing a signal;

placing said third processing element in a particular location downstream from said first processing element and upstream from said second processing element based at least in part upon its function in said processing web, various inputs to and outputs from said second processing element, and a relationship between said third processing element and said first and second processing elements so that the signal processed by the first processing ~~apparatus~~ element is forwarded to the third processing ~~apparatus~~ element in response to a request for processing from said third processing element, and the signal processed by the third processing ~~apparatus~~ element is forwarded to the second processing ~~apparatus~~ element in response to a request for processing from said second processing element;

wherein said third processing element is a parameter output.

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19. (Original) The method of claim 14, wherein said connection between said first processing element and said second processing element is provided in a color indicative of the type of data flowing therebetween.

20. (Original) The method of claim 14, wherein each of said first and second processing elements includes an indication of the number of inputs and outputs thereof.

21. (Original) The method of claim 14, wherein said inputs and outputs are provided in a color indicative of the type of data to be received or output thereon.

Claims 22-44 (Cancelled)

45. (Currently Amended) A graphical representation of a processing web by an oscilloscope for implementation and control of the oscilloscope, comprising:

a first processing element of said processing web corresponding to a first processing apparatus of the oscilloscope for processing a signal, said first processing element being placed in a particular location based at least in part upon its function and various inputs to and outputs from said first processing element;

a second processing element of said processing web corresponding to a second processing apparatus of the oscilloscope for processing a signal, said second processing element being placed in a particular location downstream from said first processing element based at least in part upon its function, various inputs to and outputs from said second processing element, and a relationship between said second processing element and said first processing element; and

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a connection for connecting said first processing element to second processing element indicating a flow of data therebetween so that ~~the signal processed by~~ in response to a request for the signal passed upstream from said second processing element the first processing ~~apparatus element is forwarded~~ processes and forwards the signal to the second processing ~~apparatus element upon a request from said second processing element to said first processing element for said signal;~~

wherein said first processing element is a waveform acquisition processing element; and wherein said second processing element is a display processing.

46. (Currently Amended) The graphical representation of the processing web of claim 45, further comprising:

a third processing element of said processing web corresponding to a third processing apparatus of the oscilloscope for processing a signal, said third processing element being placed in a particular location downstream from said first processing element and upstream from said second processing element based at least in part upon its function in said processing web, various inputs to and outputs from said second processing element, and a relationship between said third processing element and said first and second processing elements so that the signal processed by the first processing ~~apparatus element~~ is forwarded to the third processing apparatus element in response to a request for processing from said third processing element, and the signal processed by the third processing ~~apparatus element~~ is forwarded to the second processing apparatus element in response to a request for processing from said second processing element;

wherein said third processing element performs an intermediate processing step between said first processing element and said second processing element.

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47. (Currently Amended) The graphical representation of the processing web of claim 45, further comprising:

a third processing element of said processing web corresponding to a third processing apparatus of the oscilloscope for processing a signal, said third processing element being placed in a particular location downstream from said first processing element and upstream from said second processing element based at least in part upon its function in said processing web, various inputs to and outputs from said second processing element, and a relationship between said third processing element and said first and second processing elements so that the signal processed by the first processing ~~apparatus~~ element is forwarded to the third processing ~~apparatus~~ element in response to a request for processing from said third processing element, and the signal processed by the third processing ~~apparatus~~ element is forwarded to the second processing ~~apparatus~~ element in response to a request for processing from said second processing element;

wherein said third processing element is a static memory input.

48. (Currently Amended) The graphical representation of the processing web of claim 45, further comprising:

a third processing element of said processing web corresponding to a third processing apparatus of the oscilloscope for processing a signal, said third processing element being placed in a particular location downstream from said first processing element and upstream from said second processing element based at least in part upon its function in said processing web, various inputs to and outputs from said second processing element, and a relationship between said third processing element and said first and second processing elements so that the signal processed by the first processing ~~apparatus~~ element is forwarded to the third processing ~~apparatus~~ element in

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response to a request for processing from said third processing element, and the signal processed by the third processing ~~apparatus~~ element is forwarded to the second processing ~~apparatus~~ element in response to a request for processing from said second processing element;

wherein said third processing element is a display trace output including at least one processing function.

49. (Currently Amended) The graphical representation of the processing web of claim 45, further comprising:

a third processing element of said processing web corresponding to a third processing apparatus of the oscilloscope for processing a signal, said third processing element being placed in a particular location downstream from said first processing element and upstream from said second processing element based at least in part upon its function in said processing web, various inputs to and outputs from said second processing element, and a relationship between said third processing element and said first and second processing elements so that the signal processed by the first processing ~~apparatus~~ element is forwarded to the third processing ~~apparatus~~ element in response to a request for processing from said third processing element, and the signal processed by the third processing ~~apparatus~~ element is forwarded to the second processing ~~apparatus~~ element in response to a request for processing from said second processing element;

wherein said third processing element is a parameter output.

50. (Original) The graphical representation of the processing web of claim 45, wherein said connection between said first processing element and said second processing element is provided in a color indicative of the type of data flowing therebetween.

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51. (Original) The graphical representation of the processing web of claim 45, wherein each of said first and second processing elements includes an indication of the number of inputs and outputs thereof.

52. (Original) The graphical representation of the processing web of claim 51, wherein said inputs and outputs are provided in a color indicative of the type of data to be received or output thereon.

Claims 53-77 (Cancelled)